



**B V Raju Institute of Technology**  
( Sri Vishnu Educational Society )  
UGC - Autonomous, NAAC, NBA, Accredited Institute  
Affiliated to JNTU, Hyderabad.



# Department of Electronics and Communication Engineering

## M.Tech-VLSI Design

List of Placements for the A.Y:2023-2024(CAY) \_ Batch: 2022-24

S.No	Student Name	Roll No	Discipline	Year of Passing	On/Off Campus	Name of the Employer
1	Y RAMA CHANDRA SHARMA	22211D5703	VLSI Design	2024.	Off Campus	BITSILICA

**HEAD OF THE DEPARTMENT**

Head of Dept  
Electronics & Comm Engg  
Padmasri Dr. B.V. Raju Inst of Technology  
Narsapur, Medak Dist-502312

B. N. K. Ramesh



**PRINCIPAL**

**PRINCIPAL**  
B V RAJU INSTITUTE OF TECHNOLOGY  
Vishnuvar, Narsapur Medak Dist  
Telangana - 502312



## Department of Electronics and Communication Engineering

### M.Tech-VLSI Design

#### List of Placements for the A.Y:2022-2023(CAYm1) \_ Batch: 2021-23

S.No	Student Name	Roll No	Discipline	Year of Passing	On/Off Campus	Name of the Employer
1	CHAKALI KASTHURI	21211D5702	VLSI Design	2023	Off Campus	BITSILICA
2	KUMMARI LINGAMIAH	21211D5703	VLSI Design	2023	Off Campus	BITSILICA
3	SANGU SAMATHA	21211D5706	VLSI Design	2023	Off Campus	IIT HYDERABAD
4	NERELLA SHASHI KUMAR	21211D5707	VLSI Design	2023	Off Campus	BITSILICA

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B. Nagaraj Kumar



K. Prasad  
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Narsapur, Narsapur, Medak Dist  
Telangana 502313



## Department of Electronics and Communication Engineering

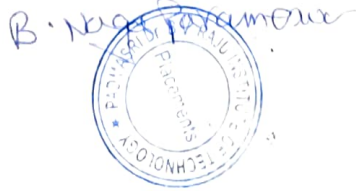
### M.Tech-VLSI Design

#### List of Placements for the A.Y:2021-2022(CAYm2) \_Batch: 2020-22

S.No	Student Name	Roll No	Discipline	Year of Passing	On/Off Campus	Name of the Employer
1	M.D.ARFATH	20211D5701	VLSI Design	2022	Off Campus	HCL
2	PULLURI BHARGAV	20211D5702	VLSI Design	2022	Off Campus	INCISE SEMICONDUCTORS
3	GARADAS MANASA	20211D5705	VLSI Design	2022	Off Campus	ACCENTURE
4	THOKALA RAJESHWAR REDDY	20211D5708	VLSI Design	2022	Off Campus	INCISE SEMICONDUCTORS
5	GUTTIKONDA SAI RAJASEKHAR REDDY	20211D5709	VLSI Design	2022	Off Campus	BITSILICA
6	EMMADI SAICHARAN	20211D5710	VLSI Design	2022	Off Campus	DPIIND SERVICES PVT LTD
7	PENDLY SAIKUMAR	20211D5712	VLSI Design	2022	Off Campus	BITSILICA
8	SRILATHA POLICE	20211D5714	VLSI Design	2022	Off Campus	MEYVNSYSTEMS PVT LTD
9	VUTUKURI VISHWAKANTH	20211D5717	VLSI Design	2022	Off Campus	BITSILICA
10	YARRAMSHETTY JAGADEESH	20211D5718	VLSI Design	2022	Off Campus	BITSILICA
11	SANJEEV KUMAR	20211D5713	VLSI Design	2022	Off Campus	BITSILICA

  
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Telangana 502313



# Department of Electronics and Communication Engineering

## M.Tech-VLSI Design

### List of Placements for the A.Y:2020-2021(CAYm3) \_ Batch: 2019-21

S.No	Student Name	Roll No	Discipline	Year of Passing	On/Off Campus	Name of the Employer
1	NAKKA AJAY	19211D5702	VLSI Design	2021	Off Campus	BITSILICA
2	BHAVANI CHENNALA	19211D5704	VLSI Design	2021	Off Campus	AMD
3	MADEM GURUVINAYKUMAR REDDY	19211D5706	VLSI Design	2021	Off Campus	ALTRON
4	BADRI INDRAJA	19211D5707	VLSI Design	2021	Off Campus	WIPRO
5	BOVILLA KARTHIK REDDY	19211D5708	VLSI Design	2021	Off Campus	MIRAFRA TECHNOLOGIES
6	B.PRANAY KUMAR GOUD	19211D5711	VLSI Design	2021	Off Campus	TCS
7	BOVILLA RAMYA	19211D5712	VLSI Design	2021	Off Campus	EXIMUS
8	M.SAIUTHEJ	19211D5713	VLSI Design	2021	Off Campus	BITSILICA
9	CH.SHANKER	19211D5714	VLSI Design	2021	Off Campus	TCS
10	SHEEMA SAADIA	19211D5716	VLSI Design	2021	Off Campus	AMD
11	D VISHNU TEJASVI	19211D5718	VLSI Design	2021	Off Campus	BITSILICA

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